

**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. (Original) A method for the autonomic configuration of cable speeds in a computing environment, the method comprising:

reading a cable identifier of an interconnection cable connecting components in the computing environment;

storing the cable identifier of the interconnection cable in a software object within the computing environment; and

adjusting port speeds of components connected by the interconnection cable based on the cable identifier.

2. (Original) The method of claim 1, wherein the method is triggered upon system bring-up.
3. (Original) The method of claim 1, wherein the method is triggered during run time when the interconnection cable becomes active.
4. (Original) The method of claim 1, wherein the cable identifier contains the length of the associated interconnection cable.
5. (Original) The method of claim 1, wherein the cable identifier contains the type of the associated interconnection cable.

6. (Original) The method of claim 4, wherein the step of adjusting port speeds of components connected by the interconnection cable based on the cable identifier further comprises the steps of:

determining the maximum port speeds of each of the components connected by the interconnection cable;

translating the cable length of the associated interconnection cable to a maximum effective transmission speed for the cable;

if the maximum port speed of any of the components connected to the interconnection cable is less than the maximum effective transmission speed of the cable, adjusting the port speed of the components to the lowest maximum port speed of the components; and

if the maximum port speed of all of the components connected to the interconnection cable is greater than or equal to the maximum effective transmission speed of the cable, adjusting the port speed of the components to the maximum effective transmission speed of the cable.

7. (Original) The method of claim 4, wherein one or more pins on the interconnection cable connector are jumpered to a first voltage supply and, in conjunction with bias resistors on the connected components, create the cable identifier.

8. (Original) The method of claim 1, wherein at least one of the components is a logically partitioned computer system.

9. (Original) The method of claim 1, wherein at least one of the components is an I/O enclosure.

10. (Original) A computer-readable program stored on a computer-readable medium, said computer readable program being configured to perform the steps of: reading a cable identifier of an interconnection cable connecting components in the computing environment;

storing the cable identifier of the interconnection cable in a software object within the computing environment; and

adjusting port speeds of components connected by the interconnection cable based on the cable identifier.

11. (Original) The computer-readable program of claim 10, wherein the method is triggered upon system bring-up.

12. (Original) The computer-readable program of claim 10, wherein the method is triggered during run time when the interconnection cable becomes active.

13. (Original) The computer-readable program of claim 10, wherein the cable identifier contains the length of the associated interconnection cable.

14. (Original) The computer-readable program of claim 13, wherein the step of adjusting port speeds of components connected by the interconnection cable based on the cable identifier further comprises the steps of:

determining the maximum port speeds of each of the components connected by the interconnection cable;

translating the cable length/type of the associated interconnection cable to a maximum effective transmission speed for the cable;

if the maximum port speed of any of the components connected to the interconnection cable is less than the maximum effective transmission speed of the cable, adjusting the port speed of the components to the lowest maximum port speed of the components; and

if the maximum port speed of all of the components connected to the interconnection cable is greater than or equal to the maximum effective transmission speed of the cable, adjusting the port speed of the components to the maximum effective transmission speed of the cable.

15. (Original) The computer readable program of claim 13, wherein additional pins on the interconnection cable connector are jumpered to a first voltage supply and, in conjunction with bias resistors on the connected components, create the cable identifier.

16. (Currently Amended) The [method] computer readable program of claim 10, wherein at least one of the components is a logically partitioned computer system.

17. (Currently Amended) The [method] computer readable program of claim 10, wherein at least one of the components is an I/O enclosure.